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Substitute for form 1449.PTO

**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**

(Use as many sheets as necessary)

Complete if Known

Application Number	10/618,462
Filing Date	07/11/2003
First Named Inventor	Guangming Yin
Art Unit	2817
Examiner Name	Patricia T. Nguyen
Attorney Docket Number	BP1817CON1

Sheet 2 of 2

NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
PN		LEUNG D L C ET AL: "A 3-V CMOS differential bandpass amplifier for GSM receivers" CIRCUITS AND SYSTEMS, 1998. ISCAS '98. PROCEEDINGS OF THE 1998 IEEE INTERNATIONAL SYMPOSIUM ON MONTEREY, CA. USA 31 MAY-3 JUNE 1998. NEW YORK, NY, USA, IEEE, US, 31 MAY 1998 (1998-05-31), pages 341-344, XPO10289447 ISBN: 0-7803-4455-3 *figure 5*	
		QIANG LI ET AL INSTITUTE OF ELECTRICAL AND ELECTRONICS ENGINEERS: "RF circuit performance degradation due to soft breakdown and hot carrier effect in 0.18 μ m CMOS technology" 2001 IEEE RADIO FREQUENCY INTEGRATED CIRCUITS (RFIC) SYMPOSIUM. DIGEST OF PAPERS. PHOENIX, AZ, MAY 20 - 22, 2001. IEEE RADIO FREQUENCY INTEGRATED CIRCUITS SYMPOSIUM, NEW YORK, NY. IEEE, US, 20 May 2001 (2001-05-20) pages 139-142, XP010551339 ISBN: 0-7803-6601-8 *figure 3*	
		RAZAVI B ED-INSTITUTE OF ELECTRICAL AND ELECTRONICS ENGINEERS: "Prospects Of CMOS Technology For High-Speed Optical Communication Circuits" GaAs IC symposium, 23rd. Annual IEEE Gallium Arsenide Integrated Circuit Symposium, Technical digest 2001. BALTIMORE, MD, OCT. 21-24, 2001, GaAs IC SYMPOSIUM-IEEE GALLIUM ARSENIDE INTEGRATED CIRCUIT SYMPOSIUM, NEW YORK, NY IEEE, US, 21 October 2001 (2001-10-21), pages 3-6, XP001046946 ISBN: 0-7803-6663-8 *figures 6a,7	
PN		LI Q ET AL: "RF Circuit Performance Degradation Due to Soft Breakdown and Hot-Carrier Effect in Deep-Submicrometer CMOS Technology" IEEE Transactions on Microwave Theory and Techniques, IEEE Inc. New York, US, vol. 49, no. 9, September 2001 (2001-09), pages 1546-1551, XP001096604 ISSN: 0018-9480 *figure 9*	

Examiner Signature	Patricia Nguyen	Date Considered	5/24/05
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Sheet	1	of	8
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PTO/SB/08A (08-03)

Approved for use through 07/31/2006. OMB 0651-0031

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U. S. PATENT DOCUMENTS

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		Number-Kind Code ² (if known)			
PN		US- 4,449,248	5-15-1984	Leslie et al	
		US- 4,519,068	05-21-1985	Krebs et al	
		US- 4,545,023	10-01-1985	Mizzi	
		US- 4,680,787	07-14-1987	Marry	
		US- 4,731,796	03-15-1988	Masterton et al	
		US- 4,737,975	04-12-1988	Shafer	
		US- 4,761,822	08-02-1988	Maile	
		US- 4,777,657	10-11-1988	Gillaspie	
		US- 4,794,649	12-27-1988	Fujiwara	
		US- 4,804,954	02-14-1989	Macnak et al	
		US- 4,807,282	02-21-1989	Kazan et al	
		US- 4,817,115	03-28-1989	Campo et al	
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		US- 4,894,792	01-16-1990	Mitchell et al	
	US- 4,916,441	04-10-1990	Gombrich		
	US- 4,964,121	10-16-1990	Moore		
	US- 4,969,206	11-06-1990	Desrochers		
PN		US- 4,977,611	12-11-1990	Maru	

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PN		US- 5,153,878	10-06-1992	Krebs	
		US- 5,175,870	12-29-1992	Mabey et al	
		US- 5,179,721	01-12-1993	Comroe et al	
		US- 5,181,200	01-19-1993	Harrison	
		US- 5,230,084	07-20-1993	Nguyen	
		US- 5,239,662	08-24-1993	Danielson et al	
		US- 5,241,542	08-31-1993	Natarajan et al	
		US- 5,241,691	08-31-1993	Owen	
		US- 5,249,220	09-28-1993	Moskowitz et al	
		US- 5,249,302	09-28-1993	Metroka et al	
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		US- 5,289,469	02-22-1994	Tanaka	
		US- 5,291,516	03-01-1994	Dixon et al	
		US- 5,293,639	03-08-1994	Wilson et al	
PN		US- 5,296,849	03-22-1994	Ide	

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		First Named Inventor	Guangming Yin
		Art Unit	2817
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PN		US- 5,297,144	03-22-1994	Gilbert et al	
		US- 5,323,392	06-21-1994	Ishii et al	
		US- 5,331,509	07-19-1994	Kikinis	
		US- 5,349,649	09-20-1994	Iijima	
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		US- 5,418,837	05-23-1995	Johansson et al	
		US- 5,423,002	06-06-1995	Hart	
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		US- 5,428,636	06-27-1995	Meier	
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		US- 5,438,329	08-01-1995	Gastouniotis et al	
		US- 5,440,560	11-07-1995	Rypinski	

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PN		US- 5,465,081	11-07-1995	Todd	
		US- 5,481,265	01-02-1996	Russell	
		US- 5,481,562	01-02-1996	Pearson et al	
		US- 5,533,029	07-02-1996	Gardner	
		US- 5,535,373	07-09-1996	Oinowich	
		US- 5,544,222	08-06-1996	Robinson et al	
		US- 5,579,487	11-26-1996	Meyerson et al	
		US- 5,584,048	12-10-1996	Wieczorek	
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		US- 5,630,061	05-13-1997	Richter et al	
		US- 5,680,633	10-21-1997	Koenck et al	
		US- 5,732,346	03-24-1998	Lazaridis et al	
		US- 5,740,366	04-14-1998	Mahany et al	
		US- 5,744,366	04-28-1998	Kricka et al	
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		US- 5,839,051	11-17-1998	Grimmett et al	
		US- 5,940,771	08-17-1999	Gollnick et al	
		US- 6,014,705	01-11-2000	Koenck et al	
PN		US- 6,374,311	04-16-2002	Mahany et al	

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PN		Bida, A Hybrid Wireless MAC Protocol Supporting Asynchronous and Synchronous MSDU Delivery Services, IEEE 802.11/91-92, September 1991, pages 1-46	
		Robert Meier's Master's Thesis, Mobile Computer Network Architecture, (May 1993), 82 pages	
		Hoberecht, A Layered network Protocol for Packet Voice and Data Integration, IEEE Journal on Selected Areas in Communications, Vol. SAC-1, No. 6 (Dec. 1983), pages 1006-1013	
		Rypinski, Architecture-Topology and Protocol Stacks, IEEE 802.11/91-21, March 1991, 12 pages	
		Cheah, A Proposed Architecture and Access Protocol Outline for the IEEE 802.11 Radio LAN Standards, Document IEEE P802.11/91-54, May 1991; pages 1-20	
		Rypinski, Power-Drain Considerations for Full Time and Sleep Mode Radio Receivers, IEEE P802.11/91-99, September 1991, 11 pages	
		Natarajan et al, Battery Efficient Operation of Radio MAC Protocol, IEEE P802.11/91-102, September 1991, pages 1-5	
		Cox, A Radio System Proposal for Widespread Low-Power Tetherless Communications, IEEE Transactions on Communications, Vol. 39, No. 2 (February 1991), pages 324-335	
		Bagby, One Approach to Wireless Network Architecture, IEEE P802.11/91-2, January 1991, 1-15, 17-21	
PN		Biba, A Modest Proposal for a Asynchronous, Data Intensive, Wireless Local Area Network, IEEE P802.11/91-25, March 1991; pages 1-25	

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PN		Heimschi et al; Merged CMOS/Bipolar Current Switch Logic (MCSL), IEEE Journal of Solid State Circuits, Vol.24, pp. 1307-11, October 1989, page 1307-1311	
PN		Elrabaa et al; Optimization of Digital BiCMOS Circuits, An Overview, IEEE 1992, 5 pages	
PN		Mudd et al, Very High Speed ECL/CMG Gate Arrays with Submicron Structures; IEEE 1989, 5 pages	

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